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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jaroslaw J. Sydir et al.

Title: Method and Apparatus for Scheduling the Processing of Commands for Execution by Cryptographic Algorithm Cores in a Programmable Network Processor

Docket No.: Intel-012PUS

Serial No.: 10/747,852

Filed: December 29, 2003

Due Date: N/A

Examiner: Unknown

Group Art Unit: 2131

MS Amendment

Commissioner for Patents

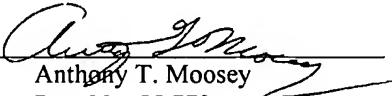
P.O. Box 1450

Alexandria, VA 22313-1450

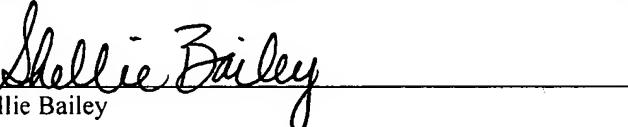
We are transmitting herewith the following attached items (as indicated with an "X"):

Return postcard and transmittal document.
 Information Disclosure Statement (2 pgs.), Form 1449 (2 pgs.), and copies of 10 cited documents.

Customer Number: 45780

By 
Anthony T. Moosey
Reg. No. 55,773

CERTIFICATE UNDER 37 CFR 1.8: The undersigned hereby certifies that this correspondence is being deposited with the United States Postal Service with sufficient postage as first class mail, in an envelope addressed to: MS Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, VA 22313-1450, on this 6th day of March, 2007.


Shéllie Bailey

(GENERAL)

S/N 10/747,852



PATENT

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

Applicant: Jaroslaw J. Sydir et al. Examiner: Unknown
Serial No.: 10/747,852 Group Art Unit: 2131
Filed: December 29, 2003 Docket: Intel-012PUS
Title: Method And Apparatus For Scheduling The Processing Of Commands For
Execution By Cryptographic Algorithm Cores In A Programmable Network
Processor

INFORMATION DISCLOSURE STATEMENT

MS Amendment
Commissioner for Patents
P.O. Box 1450
Alexandria, VA 22313-1450

In compliance with the duty imposed by 37 C.F.R. § 1.56, and in accordance with 37 C.F.R. §§ 1.97 *et. seq.*, the referenced materials are brought to the attention of the Examiner for consideration in connection with the above-identified patent application. Applicants respectfully request that this Information Disclosure Statement be entered and the documents listed on the attached Form 1449 be considered by the Examiner and made of record. Pursuant to the provisions of MPEP 609, Applicants request that a copy of the 1449 form, initialed as being considered by the Examiner, be returned to the Applicants with the next official communication.

Pursuant to 37 C.F.R. § 1.97(b), it is believed that no fee or statement is required with the Information Disclosure Statement. However, if an Office Action on the merits has been mailed, the Commissioner is hereby authorized to charge the required fees to Deposit Account No. 50-0845 in order to have this Information Disclosure Statement considered. The Examiner is invited to contact the Applicants' Representative at the below-listed telephone number if there are any questions regarding this communication.

Pursuant to 37 C.F.R. 1.98(a)(2), Applicant believes that copies of cited U.S. Patents and Published Applications are no longer required to be provided to the Office. Notification of this change was provided in the United States Patent and Trademark Office OG Notices dated October 12, 2004. Thus, Applicant has not included copies of any US Patents or Published Applications cited with this submission. Should the Office require copies to be provided,

INFORMATION DISCLOSURE STATEMENT

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Serial No :10/747,852

Dkt: P17939

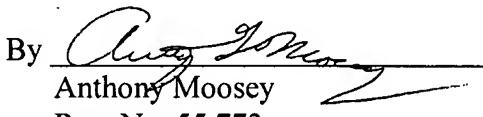
Filing Date: December 29, 2003

Title: Method and Apparatus for Scheduling the Processing of Commands for Execution by Cryptographic Algorithm Cores in a Programmable Network Processor

Applicant respectfully requests that notice of such requirement be directed to Applicant's below-signed representative. Applicant acknowledges the requirement to submit copies of foreign patent documents and non-patent literature in accordance with 37 C.F.R. 1.98(a)(2).

Respectfully submitted,

Date: March 6, 2007

By 
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Shellie Bailey



MAR 08 2007

Approved for use through 7/31/2009. OMB 0551-0231
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Substitute for form 1449A/PTO

**INFORMATION DISCLOSURE
STATEMENT BY APPLICANT**

(Use as many sheets as necessary)

Complete if Known

Application Number	10/747,852
Filing Date	December 29, 2003
First Named Inventor	Sydir, Jaroslaw
Art Unit	2131
Examiner Name	Unknown

Sheet

1 of

Attorney Docket No: Intel-012PUS

US PATENT DOCUMENTS

Examiner Initial *	Cite No	USP Document Number	Publication Date	Name of Patentee or Applicant of cited Document	Filing Date If Appropriate
		US-20020083317A1	06/27/2002	Ohta, Y. et al.	12/18/2001
		US-20030091036A1	05/15/2003	Milliken, W. C., et al.	10/03/2002
		US-20030097481A1	05/22/2003	Richter, R. K.	10/22/2002
		US-20030099254A1	05/29/2003	Richter, R. K.	10/22/2002
		US-20040004964A1	01/08/2004	Lakshmanamurthy, S. et al.	07/03/2002
		US-20040019782A1	01/29/2004	Hawkes, P. M., et al.	07/24/2002
		US-20040019783A1	01/29/2007	Hawkes, P. M., et al.	07/24/2002
		US-20040039936A1	02/26/2004	Lai, Y.	08/21/2002
		US-20040117642A1	06/17/2004	Mowery, K. R., et al.	12/17/2002
		US-20050138368A1	06/23/2005	Sydir, J. J., et al.	12/19/2003
		US-20050149744A1	07/07/2005	Sydir, J. et al.	12/29/2003
		US-3627928	12/14/1971	Wolper, D. F., et al.	02/04/1969
		US-3868631	02/25/1975	Morgan, B. O., et al.	08/10/1973
		US-4107458	08/15/1978	Constant, J. M.	08/23/1976
		US-4434322	02/28/1984	Ferrell, P. J.	07/23/1981
		US-4661657	04/28/1987	Grenzebach, K. et al.	04/11/1983
		US-5307459	04/26/1994	Petersen, B. et al.	07/28/1992
		US-5377270	12/27/1994	Koopman, Jr., P. J., et al.	06/30/1993
		US-5790545	08/04/1998	Holt, C. S., et al.	03/14/1996
		US-5860072	01/12/1999	Schofield, A.	07/11/1996
		US-6061449	05/09/2000	Candelore, B. et al.	10/10/1997
		US-6061779	05/09/2000	Garde, D.	01/16/1998
		US-6363444	03/26/2002	Platko, J. J., et al.	12/17/1999
		US-6625150	09/23/2003	Yu, JungJi J.	12/16/1999
		US-6757791	06/29/2004	O'Grady, R. et al.	03/30/1999
		US-6829315	12/07/2004	Cruikshank, B. S.	01/19/2000
		US-6853635	02/08/2005	Beshai, M. E.	07/24/2000
		US-6971006	11/29/2005	Krishna, S. et al.	08/23/2002
		US-7069447	06/27/2006	Corder, R. J.	05/10/2002
		US-7073067	07/04/2006	Mizrah, L. L.	05/07/2003
		US-7082534	07/25/2006	Tardo, J.	05/31/2002

FOREIGN PATENT DOCUMENTS

Examiner Initials*	Cite No	Foreign Document No	Publication Date	Name of Patentee or Applicant of cited Document	T ²
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EXAMINER

DATE CONSIDERED

Substitute Disclosure Statement Form (PTO-1449)

* EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 809. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant. 1 Applicant's unique citation designation number (optional) 2 Applicant is to place a check mark here if English language Translation is attached

Substitute for form 1449A/PTO			
INFORMATION DISCLOSURE STATEMENT BY APPLICANT			
(Use as many sheets as necessary)			
Sheet	2	of	2
Attorney Docket No: Intel-012PUS			

OTHER DOCUMENTS -- NON PATENT LITERATURE DOCUMENTS			
Examiner Initials*	Cite No ¹	Include name of the author (in CAPITAL LETTERS), title of the article (when appropriate), title of the item (book, magazine, journal, serial, symposium, catalog, etc.), date, page(s), volume-issue number(s), publisher, city and/or country where published.	T ²
		"Intel IXP2850 Network Processor: High-Speed, Secure Content Processing in a Single Chip", <u>Intel Corporation 2002</u> , http://www.intel.com/design/network/prodbrf/25213601.pdf , 1-6	
		"Product Brief: Intel IXP2855 Network Processor", <u>Intel Corporation: intel.com/go/networkprocessors</u> , (2005), 8	
		"System Packet Interface Level 4 (SPL-4) Phase 2 Revision 1: OC-192 System Interface for Physical and Link Layer Devices", <u>Optical Internetworking Forum</u> , (October 15, 2003), 72	
		ACHARYA, M. , et al., "Secure Comparison of Encrypted Data in Wireless Sensor Networks", (2002), 7	
		BERGEN, C. , et al., "Streaming Interface (NPSI) Implementation Agreement", <u>The Network Processing Forum</u> , (October 18, 2002), 87	
		CHOU, W. , "Inside SSL: Accelerating Secure Transactions", <u>IEEE, IT PRO</u> , (September 2002), 37-41	
		PALKERT, T. , "Optical Internetworking Forum Report", <u>Network Elements Incorporated</u> , (November 4, 1999), 15	
		PEREIRA, R. , et al., "The ESP CBC-Mode Cipher Algorithms", <u>Network Working Group Request for Comments: 2451</u> , http://www.ietf.org/rfc/rfc2451.txt , (November 1998), 14	
		RAMASWAMY, R. , et al., "Considering Processing Cost in Network Simulations", <u>ACM SIGCOMM Workshop</u> , (August 2003), 47-56	
		VENKATACHALAM, M. , "Integrated Data and Control Plane Processing Using Intel(R) IXP23XX Network Processors", <u>Technology@Intel Magazine</u> , (February 2005), 11	

EXAMINER

DATE CONSIDERED